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| 10/772,815   | 02/05/2004  | Shugong Xu           | SLA1535             | 5959             |
| 7590<br>Gerald W. Maliszewski<br>P.O. Box 270829<br>San Diego, CA 92198-2829 |             |                      |                     |                  |
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| SIDDIQI, MOHAMMAD A  |             |                      |                     |                  |
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

### Office Action Summary

**Application No.**

10/772,815

**Applicant(s)**

XU, SHUGONG

**Examiner**

MOHAMMAD A. SIDDIQI

**Art Unit**

2154

**Period for Reply** -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 15 April 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-4, 7, 8, 11-15, 18, 19, 22-25, 28, 31-35, 38 and 41-44 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 7-8, 11-15, 18-19, 22-25, 28, 31-35, 38 and 41-44 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 04/15/2008 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-848)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_

### **DETAILED ACTION**

1. Claims 1-4,7-8,11-15,18-19,22-25, 28,31-35, 38,and 41-44 are presented for examination. Claims 41-44 are new. Claims 5-6, 9-10, 16-17, 20-21, 26-27, 29-30, 36-37, and 39-40 have been cancelled.

### ***Claim Objections***

2. Claim 44 objected to because of the following informalities: The claim 44 depends on the cancelled claim 20. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 101***

3. Independent claims 22, 32 and their dependent claims are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter. The language of the claims raises a question as to whether the claims are directed merely to an abstract idea that is not tied to a technological art, environment or machine which would result in a practical application producing a concrete, useful and tangible result. The various steps receiver and de-jitter module are software constructs performing various functionalities. These functionalities do not manipulate any hardware

or tangible entity. Therefore, these software constructs are non statutory entities as detailed in MPEP 2106.

***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

5. Claims 1, 3,4, 7,8, 11,12, 14,15, 18,19,22, 24,25, 28, 31,32, 34,35, 38, and 40-44 are rejected under 35 U.S.C. 102(e) as being anticipated by Ueda et al (US 2004/0190459) (hereinafter Ueda).

6. As per claim 1, Ueda discloses a method for receiving an MPEG2 transport stream (TS) in a real-time protocol (RTP)/user datagram protocol (UDP)/Internet protocol (IP) packet, the method comprising:

receiving (130, fig 1, para #0073) an IP packet via an IP network, having a variable transmission delay (para #0003; #007);

accessing a timestamp carried in a RTP packet (para #0003; #009; para #0074);  
accessing an index field in the RTP packet header (504, fig 25, para #0095);  
linking the timestamp with a program clock reference (PCR) MPEG2TS carried in the RTP packet payload (para #00123) by using the index to point to a PCR MPEG2TS randomly positioned (storage area is managed by using indexes, para#0095) in the RTP packet payload (fig 4, para #0095; #0099 ); and,  
using the timestamp to eliminate variable transmission delay jitter, associated with the PCR MPEG2TS (para #0003; #009-#0010; para #0119, minimized).

7. As per claim 3, Ueda discloses accessing a timestamp carried in the RTP packet includes accessing a RTP timestamp carried in a RTP packet header (para #0074).

8. As per claim 4, Ueda discloses linking the timestamp with a PCR MPEG2TS carried in the RTP packet payload includes linking the RTP timestamp with a solitary PCR MPEG2TS carried in the RTP packet payload (fig 25, para #0010; page 5, para #0074-#0075) .

9. As per claim 7, Ueda discloses accessing an index field in the RTP packet header includes accessing a timestamp packet index field (504, fig 25, , para #0009-#0010; para #0074-#0075); and, wherein linking the timestamp with a PCR MPEG2TS carried in the RTP packet payload includes using the timestamp packet index to link an RTP timestamp to a PCR MPEG2TS randomly positioned in the RTP packet payload

(fig 25, para #0009-#0010; para #0074-#0075).

10. As per claim 8, Ueda discloses accessing an index field in the RTP packet header includes accessing a PCR MPEG2TS index field (504, fig 25, para #0009-#0010; para #0074-#0075); wherein accessing a timestamp carried in the RTP packet includes accessing a PCR MPEG2TS timestamp carried in a RTP packet header; and, wherein linking the timestamp with a PCR MPEG2TS carried in the RTP packet payload includes using the PCR MPEG2TS index to point to a PCR MPEG2TS randomly positioned in the RTP packet payload (504, fig 25, para #0009-#0010; page 5, para #0074-#0075).

11. As per claim 11, Ueda discloses using the timestamp to eliminate variable transmission delay jitter, associated with the PCR MPEG2TS, includes using the timestamp to determine the target transmission time of the PCR MPEG2TS (para#0003; para #0009-#0010; para #0074-#0075).

12. As per claim 12, Ueda discloses a method for transmitting an MPEG2 transport stream (TS) in a real-time protocol (RTP)/user datagram protocol (UDP)/Internet protocol (IP) packet, the method comprising: encapsulating a program clock reference (PCR) MPEG2TS in the RTP packet payload (para#0003; para #0009-#0010; page 5, para #0074-#0075);

encapsulating a timestamp randomly positioned in a RTP packet payload, referencing the PCR MPEG2TS target transmission time (130, fig 4, para #0009-#0010); encapsulating the RTP packet in an IP packet (para#0003; para #0009-#0010para #0074-#0075); encapsulating an index in the RTP packet header pointing to the position of the MPEG2TS in the RTP packet payload (storage area is managed by using indexes, para#0095); and transmitting the IP packet via an IP network (para#0003; para #0009-#0010; para #0074-#0075).

13. As per claim 14, Ueda discloses encapsulating a timestamp in a RTP packet includes encapsulating an RTP timestamp in the RTP packet header (para #0074).

14. As per claim 15, Ueda discloses encapsulating a PCR MPEG2TS in the RTP packet payload includes encapsulating a solitary PCR MPEG2TS in the RTP packet payload (fig 25, para #0009-#0010; para #0074-#0075).

15. As per claim 18, Ueda discloses encapsulating a timestamp in a RTP packet includes encapsulating an RTP timestamp in the RTP packet header (para #0009-#0010; para #0074-#0075; page 9, para#0122-#0123); and, wherein encapsulating an index in the RTP packet header includes encapsulating a timestamp packet index in the RTP packet header (para #0009-#0010; page 5, para #0074-#0075; page 9,

para#0122-#0123).

16. As per claim 19, Ueda discloses encapsulating a timestamp in the RTP packet includes encapsulating a PCR MPEG2TS timestamp (para #0009-#0010; para #0074-#0075; para#0122-#0123); and, wherein encapsulating an index in the RTP packet header includes encapsulating a PCR MPEG2TS index field in the RTP packet header (para #0009-#0010; para #0074-#0075; page 9, para#0122-#0123).

17. As per claims 22, 24, 25, 28 and 31, claims are rejected for the same reasons as claims 1, and 3, 4, 7, 8 and 11 above.

18. As per claims 32, 34, 35, 38, claims are rejected for the same reasons as claims 12, and 14, 15, 18, and 19 above.

19. As per claim 41, Ueda discloses a method for receiving an MPEG2 transport stream (TS) in a real-time protocol (RTP)/user datagram protocol (UDP)/Internet protocol (IP) packet, the method comprising:  
receiving (130, fig 1, para #0073) an IP packet via an IP network, having a variable transmission delay (para #0003; #007);  
accessing a local timestamp field in an MPEG2TS delay compensation data structure (fig 2, para #0003; #009; para #0074), where the MPEG2TS delay compensation data



structure is carried in the RTP packet payload and includes the local timestamp and a corresponding PCR MPEG2TS (fig 2, para #0088); linking the timestamp with a program clock reference (PCR) MPEG2TS carried in the RTP packet payload (para #00123); using the timestamp to eliminate variable transmission delay jitter, associated with the PCR MPEG2TS (para #0003; #009-#0010; para #0119, minimized); and, wherein linking the timestamp with a PCR MPEG2TS carried in the RTP packet includes linking the local timestamp to the corresponding PCR MPEG2TS in the MPEG2TS delay compensation data structure (130, fog4, para # 0009; para #0095).

20. As per claim 42, Ueda discloses wherein accessing a local timestamp field in an MPEG2TS delay compensation data structure includes accessing a local timestamp field in each of a plurality of MPEG2TS delay compensation data structures carried in the RTP packet payload, where the MPEG2TS delay compensation data structures include an MPEG2TS selected from the group including PCR and non-PCR MPEG2TSs (payload type field anticipates more than PCR data, fig 2, para #0090; wherein linking the timestamp with a PCR MPEG2TS carried in the RTP packet payload additionally includes linking local timestamps with corresponding non-PCR MPEG2TSs (RTP packets, fig 4, para #0095 and para #0099); and, wherein using the timestamp to eliminate variable transmission delay jitter, associated with the PCR MPEG2TS, additionally includes using the local timestamps to eliminate

jitter associated with corresponding non-PCR MPEG2TSs ( para #0003; #009-  
#0010; para #0119, minimized).

21. As per claim 43, Ueda discloses a method for transmitting an MPEG2 transport stream (TS) in a real-time protocol (RTP)/user datagram protocol (UDP)/Internet protocol (IP) packet, the method comprising:  
encapsulating a program clock reference (PCR) MPEG2TS in an MPEG2TS delay compensation structure (fig 2, para #0009-0010; para #0088), carried in the RTP packet payload (fig 2, para #0088);  
encapsulating a timestamp in a RTP packet (fig 2, para #0088), referencing the PCR MPEG2TS target transmission time (fig 2, para #0009-0010; para #0088);  
encapsulating the RTP packet in an IP packet; transmitting the IP packet via an IP network; and, wherein encapsulating a timestamp in the RTP packet includes encapsulating a local timestamp in the MPEG2TS delay compensation data structure (fig 2, para #0009-0010; para #0088), referencing the co-encapsulated PCR MPEG2TS (fig 2, para #0009-0010; para #0088).

22. As per claim 44, Ueda discloses wherein encapsulating the PCR MPEG2TS in an MPEG2TS delay compensation structure includes encapsulating a plurality of MPEG2TSs (fig 4, para #0009-0010; para #0088), selected from the group including PCR MPEG2TSs and a non-PCR MPEG2TSs, in a corresponding plurality of MPEG2TS delay compensation structures (fig 2, para #0009-0010; para #0088); and,

wherein encapsulating a local timestamp field in the MPEG2TS delay compensation data structure includes encapsulating a local timestamp field in each MPEG2TS delay compensation structure, referencing a co-encapsulated MPEG2TS (fig 2, para #0009-0010; para #0088; para #0095).

***Claim Rejections - 35 USC § 103***

23. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

24. Claims 2, 13, 23 and 33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ueda et al (US 2004/0190459) (hereinafter Ueda) in view of Ando et. al. (7,274,863) (hereinafter Ando).

25. As per claims 2, 13, 23, and 33 Although Ueda shows transmitting and receiving MPEG2 transport stream using real time protocol, further discloses RTP timestamp included in the RTP header. Ueda does not explicitly disclose accessing the timestamp carried in the RTP packet includes accessing a timestamp having a resolution of greater than 500 nanoseconds (ns); and, wherein using the timestamp to eliminate variable

transmission delay jitter, associated with the PCR MPEG2TS, includes reducing the jitter to less than 500 ns. However, accessing the timestamp carried in the RTP packet includes accessing a timestamp having a resolution of greater than 500 nanoseconds would be the obvious modification to system taught by Ueda. For example, Ando discloses accessing the timestamp carried in the RTP packet includes accessing a timestamp having a resolution of greater than 500 nanoseconds (ns) (col 1, lines 33-43); and, wherein using the timestamp to eliminate variable transmission delay jitter, associated with the PCR MPEG2TS, includes reducing the jitter to less than 500 ns (col 1, lines 33-43). It would have been an obvious modification to the system disclosed by Ueda to include the teachings of Ando to synchronize the RTP timestamp to the value stored in the TS packet.

### ***Response to Arguments***

26. Applicant's arguments filed 04/15/2008 have been fully considered but they are not persuasive, therefore rejections to claims 1-4,7-8,11-15,18-19,22-25, 28,31-35, and 38 is maintained.

27. Rejection to Independent claims 22, 32 and their dependent claims under 35 U.S.C. 101 is maintained. The language of the claims raises a question as to whether the claims are directed merely to an abstract idea that is not tied to a technological art, environment or machine which would result in a practical application producing a concrete, useful and tangible result. The various steps receiver and de-jitter module are

software constructs performing various functionalities. These functionalities do not manipulate any hardware or tangible entity. Therefore, these software constructs are non statutory entities. Applicant is requested to point disclosure where any hardware element (memory, computer readable or storage media) has been manipulated.

28. In the light of the forgoing discussion, the Examiner's conclusion is that the subject matter defined by the instant claims would have been obvious within the meaning of 35 U.S.C. 103(a). In response to Applicant's arguments **against the references individually**, one cannot show non-obviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986). In this case Ueda disclose accessing an index field in the RTP packet header (504, fig 25, para #0095) or that uses the index to point to a PCR MPEG2TS randomly positioned (storage area is managed by using indexes, para#0095) in the RTP packet payload (fig 4, para #0095; #0099 ). Ando discloses accessing the timestamp carried in the RTP packet includes accessing a timestamp having a resolution of greater than 500 nanoseconds (ns) (col 1, lines 33-43); and, wherein using the timestamp to eliminate variable transmission delay jitter, associated with the PCR MPEG2TS, includes reducing the jitter to less than 500 ns (col 1, lines 33-43). It would have been an obvious modification to the system disclosed by Ueda to include the teachings of Ando to synchronize the RTP timestamp to the value stored in the TS packet.

29. In the remarks applicants argued that:

**Argument:** Ueda does not disclose accessing an index field in the RTP packet header or that uses the index to point to a PCR MPEG2TS randomly positioned in the RTP packet payload.

**Response:** Ueda disclose accessing an index field in the RTP packet header (504, fig 25, para #0095) or that uses the index to point to a PCR MPEG2TS randomly positioned (storage area is managed by using indexes, para#0095) in the RTP packet payload (fig 4, para #0095; #0099 ).

### ***Conclusion***

30. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MOHAMMAD A. SIDDIQI whose telephone number is (571)272-3976. The examiner can normally be reached on Monday -Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J. Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

MAS

/Nathan J. Flynn/

Supervisory Patent Examiner, Art Unit 2154